

**Workshop/Session Title: Session on Modern Integrated Circuits and Systems (MICS)**

Date: 8-10 May 2017

Venue: Roses Beach Hotel , Monastir

Organised by IEEE ICEMIS 17 and University of Monastir

**Workshop/Session Aim:**

The aim of the session is to provide an exchange platform for academics, engineers and researchers to disseminate interact and share their knowledge and research results in the field of electronics and microelectronics. This session focuses on emerging technologies in theoretical and practical applications in the following domains:

- Digital Circuits and Systems Design
- High Performance Systems on Chip (SoC)
- communication & signal processing
- Coding and Cryptography
- Analog/RF and Mixed Signal Circuits
- Antennas and propagation
- Low Voltage and Low Power systems (LV/LP)
- Nano and Micro systems (NEMS/MEMS)
- Nano Devices, memory and circuits
- Wide band gap semiconductor Devices : modeling & simulation
- Smart Power Electronics and Micro Energy Source Harvesting

**Workshop/Scientific Committee:**

1. Abdellatif MTIBAA, ENIM, University of Monastir, Tunisia
2. Adel KALBOUSSI, FSM, University of Monastir, Tunisia
3. Anis SAKLY, ENIM, University of Monastir, Tunisia
4. Chokri SOUANI, ISSATsousse, University of Sousse, Tunisia
5. Fayçal DJEFFAL, University of Batna, Algeria
6. Fayrouz HADDAD, IM2NP, University of Marseille, France
7. Kamel BESBES, CRMN , Sousse, Tunisia
8. Mohamed ATRI, FSM, University of Monastir, Tunisia
9. Mohsen MACHHOUT, FSM, University of Monastir, Tunisia
10. Nacer ABOUCHI, CPE Lyon, University of Lyon, France.
11. Nouredine BOULEJFEN, CRMN, Sousse, Tunisia
12. Nouri MASMOUDI, ENIS, University of Sfax, Tunisia
13. Rached GHARBI, ENSIT, University of Tunis, Tunisia

**Workshop/Session Organizer (Chair):** Dr. Sami GHEDIRA

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Note: Number of papers/posters should be at least 7 for each Workshop/Session.